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Substitute for form 1449/PTO		Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/681,244		
		Filing Date	October 9, 2003		
		First Named Inventor	Howard A. Baumer		
		Art Unit	2117		
		Examiner Name	Gandhi, Dipakkumar B.		
Sheet	1	of	2	Attorney Docket Number	1875.4430000

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
/DG/	US1	4,627,070	12-02-1986	Champlin et al.	
	US2	4,833,605	05-23-1989	Terada et al.	
	US3	4,833,695	05-23-1989	Greub	
	US4	5,793,990	08-11-1998	Jirgal et al.	
	US5	5,933,021	08-03-1999	Mohd	
	US6	6,259,693	07-10-2001	Ganmukhi et al.	
	US7	6,137,734	12-24-2000	Schoner et al.	
	US8	6,317,804 B1	11-13-2001	Levy et al.	
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	US11	7,035,228 B2	04-25-2006	Baumer	
	US12	2001/0015664 A1	08-23-2001	Taniguchi	
	US13	2001/0021953 A1	09-13-2001	Nakashima	
	US14	2001/0017595 A1	08-30-2001	Cliff et al.	
	US15	2003/0120791 A1	06-26-2003	Weber et al.	
	US16	2003/0172332 A1	09-11-2003	Rearick et al.	
	US17	2004/0088443 A1	05-06-2004	Tran et al.	
	US18	2004/0141497 A1	07-22-2004	Amirichimeh et al.	
	US19	2004/0141531 A1	07-22-2004	Amirichimeh et al.	
/DG/	US20	2004/0117698 A1	06-17-2004	Tran et al.	

FOREIGN PATENT DOCUMENTS					
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)			
	FP1				
	FP2				
	FP3				
	FP4				
	FP5				
	FP6				
	FP7				
	FP8				
	FP9				

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		Number-Kind Code ² (If Known)			
/DG/	US21	2005/0190690 A1	09-01-2005	Tran	
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	US23				
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	US40				

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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	FP10					
	FP11					
	FP12					
	FP13					
	FP14					
	FP15					
	FP16					
	FP17					
	FP18					

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
/DG/	NPL1	Copy of International Search Report Issued Dec. 14, 2004 for International Application No. PCT/US03/34234, 5 pages.	
	NPL2	Alaska Quad-Port Gigabit Ethernet Over Copper Transceivers, from http://www.marvell.com/products/transceivers/quadport/... , 1 page, printed May 8, 2007.	
	NPL3	Alaska X10 Gigabit Ethernet Alaska Gigabit Ethernet Fast Ethernet Physical Layer (PHY) Transceiver Families Provide a Full Range of Ethernet Transceiver Solutions for the Broadband Communications Industry, from http://www.nuhorizons.com/FeaturedProducts/Volume3/Marvell/phy_transceiver.asp , 4 pages, Copyright 2007, printed May 8, 2007.	
	NPL4	Cisco Search: Results for "cdl" within "News@Cisco", from http://www.cisco.com/pcgi-bin/search/search.pl , Copyright 1992-2003, printed July 29, 2003.	
	NPL5	Industry Breakthrough: Marvell Announces the First Quad-Port Transceiver to Support Both Copper and Fiber-Optic Gigabit Ethernet Interfaces, from Business Wire at www.encyclopedia.com/printable.aspx?id=1G1:68912211 , 4 pages, January 9, 2001, printed May 8, 2007.	
	NPL6	David Maliniak (ed.), Bel's integrated connector modules support Marvell's Alaska quad Gigabit Ethernet transceiver, from http://www.electronicsweb.com/Content/news/... , 1 page, December 14, 2000, printed May 8, 2007.	
	NPL7	Marvell Gets Small, from http://www.lightreading.com/document.asp?doc_id=12004&print=true , 1 page, February 19, 2002, printed May 8, 2007.	
	NPL8	Marvell Introduces the Industry's Smallest Quad-Port Gigabit Transceiver Device, Enabling Ultra High Port Density Enterprise Switching Systems, from http://www.marvell.com/press/pressNewsDisplay.do?releaseID=41 , 3 pages, February 19, 2002, printed May 8, 2007.	
	NPL9	Ed Turner, David Law, Sep. 17-19, 2001, "IEEE P802.3ae MDC/MDIO", http://www.ieee802.org/3/efm/public/sep01/turner.sub.-1.sub.-0901.pdf (copy attached).	
/DG/	NPL10	Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN, from http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html , 3 pages, Copyright 1992-2003, printed July 29, 2003.	

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/DG/	NPL11	IEEE Std. 802.3, Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) access method and physical layer specifications, Section Two, pp. 9-55, Copyright 2002.	
	NPL12	IEEE Std. 802.3, Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) access method and physical layer specifications, Section Four, pp. 9-152, Copyright 2005.	
	NPL13	Cisco Systems, Hiroshi Suzuki: Taking Ethernet Beyond the LAN, http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_profile.html	
	NPL14	Cisco Systems, Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN, http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html	
	NPL15	Cisco Systems, Innovators Profiles, http://newsroom.cisco.com/dlls/innovators/inn_profiles.html	
	NPL16	Cisco Systems, Optical, http://newsroom.cisco.com/dlls/innovatros/optical/	
	NPL17	Cisco Systems, Cisco Systems ad Aretae Interactive Launch Internet Powered Property (IP2), http://newroom.cisco.com/dlls/global/asiapac/news/2000/pr_07-25.html .	
	NPL18	"Broadcom Announces the First 0.13-micron CMOS Multi-Rate Gigabit Octal Serializers/Deserializers" [online], Broadcom Press Release, October 29, 2002 [retrieved on 2003-07-16]. Retrieved from the Internet: <URL: http://www.broadcom.com/cgi-bin/pr/prps.cgi?pr_id=PR021029 > (5 pages).	
	NPL19	BCM8040 8-Channel Multi-rate 1.0-3.2-GBPS Retimer Switch [online], Broadcom Products Site Guide [retrieved on 2003-07-16]. Retrieved from the Internet: <URL: http://www.broadcom.com/products/8040.html > (2 pages).	
/DG/	NPL20	8-Channel Multi-Rate CMOS Retimer with Full Redundancy [online], BCM8040 Product Brief, Broadcom Corporation (2002) [retrieved on 2003-07-16]. Retrieved from the Internet:<URL: http://www.broadcom.com/products/8040.html > (2 pages).	

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/DG/	NPL21	BCM8020 8-Channel Multi-rate 1.0-3.2-GBPS Transceiver [online], Broadcom Products Site Guide [retrieved on 2003-07-16]. Retrieved from the Internet: <URL: http://www.broadcom.com/products/8020.html > (2 pages).	
↓	NPL22	8-Channel Multi-Rate CMOS Transceiver with Full Redundancy [online], BCM8020 Product Brief, Broadcom Corporation (2002) [retrieved on 2003-07-16]. Retrieved from the Internet: <URL: http://www.broadcom.com/products/8020.html > (2 pages).	
	NPL23	BCM8021 4-Channel Multi-rate 1.0-3.2-GBPS Transceiver with High-Speed Redundancy [online], Broadcom Products Site Guide [retrieved on 2003-07-16]. Retrieved from the Internet: <URL: http://www.broadcom.com/products/8021.html > (2 pages).	
	NPL24	4-Channel Multirate 1.0-3.2-GBPS Transceiver with High-Speed Redundancy [online], BCM8021 Product Brief, Broadcom Corporation (2002) [retrieved on 2003-07-16]. Retrieved from the Internet: <URL: http://www.broadcom.com/products/8021.html > (2 pages).	
	/DG/	NPL25	Broadcom Announces the Industry's First 0.13-micron CMOS Advanced Transceiver Solution with Signal Conditioning for Low-Cost 10-Gigabit Over Copper Links [online], Broadcom Press Release, April 29, 2003 [retrieved on 2003-07-16]. Retrieved from the Internet: <URL: http://www.broadcom.com/cgi-bin/pr/prps.cgi?pr_id=PR030429A > (4 pages).
	NPL26		
	NPL27		
	NPL28		
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	NPL30		

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